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WIDE-BAND VIDEO AMPLIFIER FEATURING

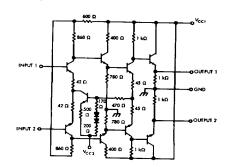
Flat Frequency Response with Low Phase-Shift from DC to 40 MHz

description

This wide-band video amplifier features a flat frequency response and low phase-shift from dc to 40 MHz. Differential inputs and outputs are provided which permit it to be used as a high-frequency differential amplifier.

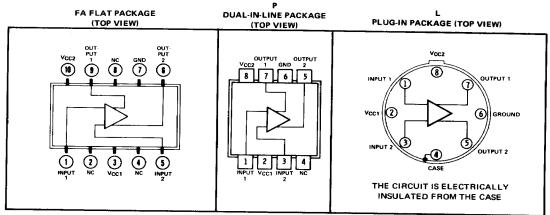
Elements of the SN7510 video-amplifier bar include transistors with transition frequency as high as 1.2 GHz under low-current and low-VCE conditions. Circuit frequency response from dc to greater than 100 MHz is possible.

schematic



Component values shown are nominal.

terminal assignments



NC-No internal connection

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltages (See Note 1): V _{CC1}															+8 V
V _{CCz}															−8 V
Differential input voltage															5 V
Positive input voltage (See Note 1).															V _{CC1}
Negative input voltage (See Note 1)															VCC2
Operating free-air temperature range													0°	'C t	o 70° C
Storage temperature range				_								-65	90	to	150°C

MOTE 1: These voltage values are with respect to network ground.

electrical characteristics, $T_A = 25^{\circ}C$, $V_{CC1} = +6$ V, $V_{CC2} = -6$ V

	PARAMETER	TEST FIGURE	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _{DO}	Differential-output offset voltage	1			0.5	2	٧
V _{CMO(av)}	Average common-mode output offset voltage	1		2	3	4	٧
l _{in}	Input current	1			50	100	μA
IDI	Differential-input offset current	1		ļ.,	5	30	μΑ
Y _{OM}	Maximum peak-to-peak output voltage	2	Single-ended, load resistance = 5 ku, f = 100 kHz, V _{in} = 20 mV rms		4.5		٧
D _S	Single-ended output distortion	2	Load resistance = $5 \text{ k}\Omega$, input distortion $\leq 0.2\%$, $V_{\text{out}} = 1 \text{ V rms}$, $f = 10 \text{ kHz}$		2		%
V _{N(in)}	Equivalent average input noise voltage	3	Single-ended, $R_S = 0$, $f = 10 \text{ Hz}$ to 500 kHz		5		μ٧
V _{CMIM}	Maximum common-mode input				±١		v
A _{vs}	Small-signal voltage gain	2	Single-ended, load resistance = 5 k Ω , f = 100 kHz	60	90	120	
A _{vom}	Common-mode-input voltage gain	4	Single-ended, load resistance = $5 \text{ k}\Omega$, $V_{\text{in}} = 0.3 \text{ V rms}$, $f = 100 \text{ kHz}$		-40	-20	dB
CMRR	Common-mode rejection ratio	4	Load resistance = $5 \text{ k}\Omega$, f = 100 kHz		80		d₿
BW	Bandwidth (—3 dB)	2			40		MHz
r _{in}	Input resistance	5	f = 100 kHz		6		kΩ
Cin	Input capacitance	5	f = 100 kHz	1	7		рF
Zout	Output impedance	5	f = 100 kHz	<u> </u>	35		Ω
P _T	Total power dissipation	1	No input signal, no external load		165	220	mW
t _r	Rise time	6	Single-ended, V _{in} = 5 mV		10	15	ns
1,	Fall time	6	Single-ended, V _{in} = 5 mV		10	15	ns

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letter symbol and parameter definitions

The d-c differential voltage that exists between the output terminals when the input terminals are at ground.

V_{CMO[av]} The average of the d-c output voltages with respect to ground when the input terminals are grounded.

IDI The difference in the currents into the two input terminals.

V_{OM} The maximum peak-to-peak autput voltage swing that can be obtained without clipping.

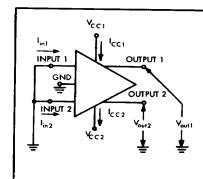
The maximum common-mode voltage that can be impressed on the input terminals while maintaining differential **VCMIM**

CMRR The ratio of the differential-mode voltage gain to the common-mode voltage gain.

₿W The range of frequencies within which the open-loop voltage gain is within 3 dB of the mid-frequency value.

PARAMETER MEASUREMENT INFORMATION

test circuits

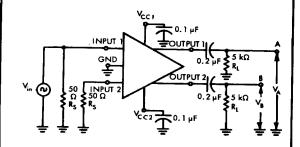


- 1. $\mathbf{V}_{DO} = |\mathbf{V}_{out1} \mathbf{V}_{out2}|$
- 2. $V_{CMO\{av\}} = \frac{V_{out1} + V_{out2}}{2}$
- 3. $I_{DI} = |I_{in1} I_{in2}|$

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4. $P_T = |V_{CC1} \cdot I_{CC1}| + |V_{CC2} \cdot I_{CC2}|$

FIGURE 1

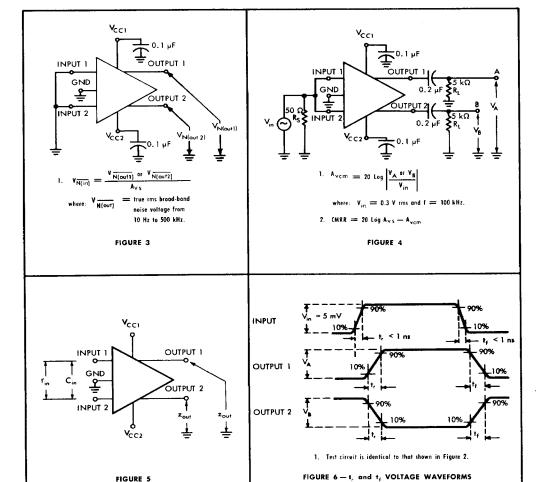


- 1. Single-ended output distortion is measured at A or B with V_A or $V_B = 1 \text{ V rms}$, input distortion < 0.2%.

- FIGURE 2

PARAMETER MEASUREMENT INFORMATION

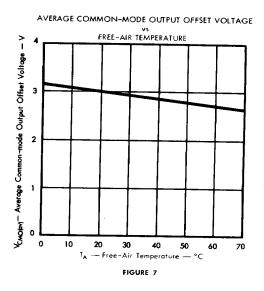
test circuits (continued)

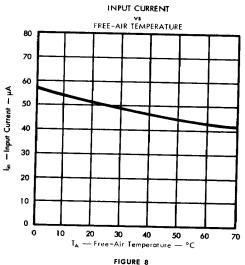


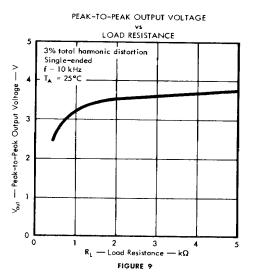
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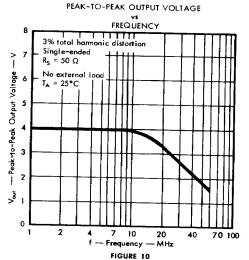
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TYPICAL CHARACTERISTICS[†]







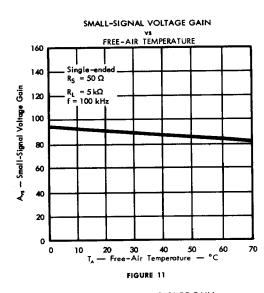


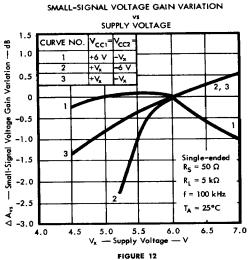
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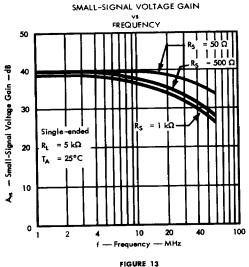
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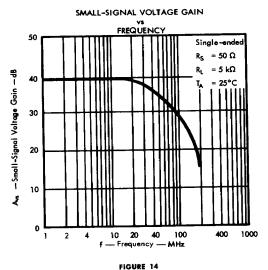
[†] Unless otherwise noted ${
m V_{CC1}}=+6$ V, ${
m V_{CC2}}=-6$ V.

TYPICAL CHARACTERISTICS†







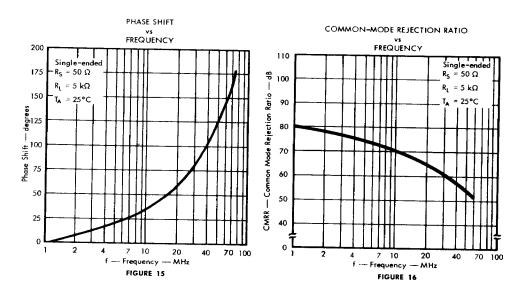


t Unless otherwise noted $\rm V_{CC1}=~+6~V,~V_{CC2}=-6~V.$

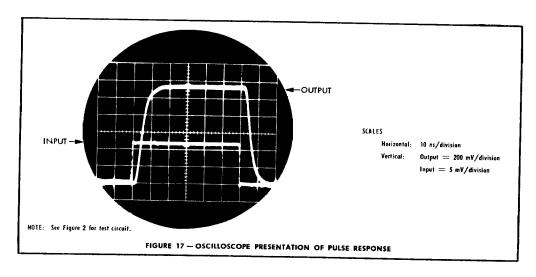
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TYPICAL CHARACTERISTICS[†]



† $\rm v_{CC1} = +6~V$ and $\rm v_{CC2} = -6~V$.



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